



Fig. 1

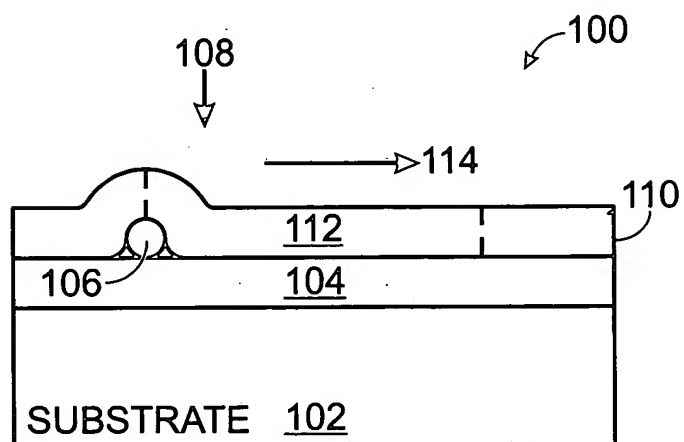


Fig. 2

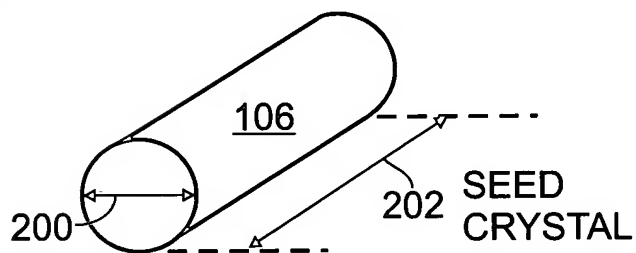


Fig. 3

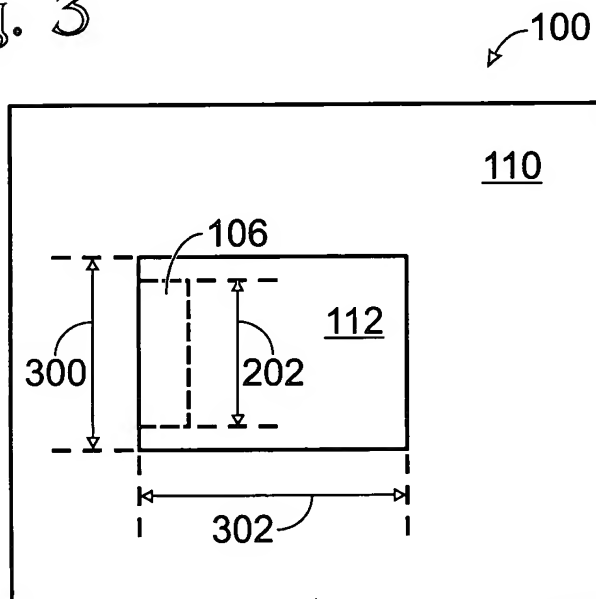


Fig. 4

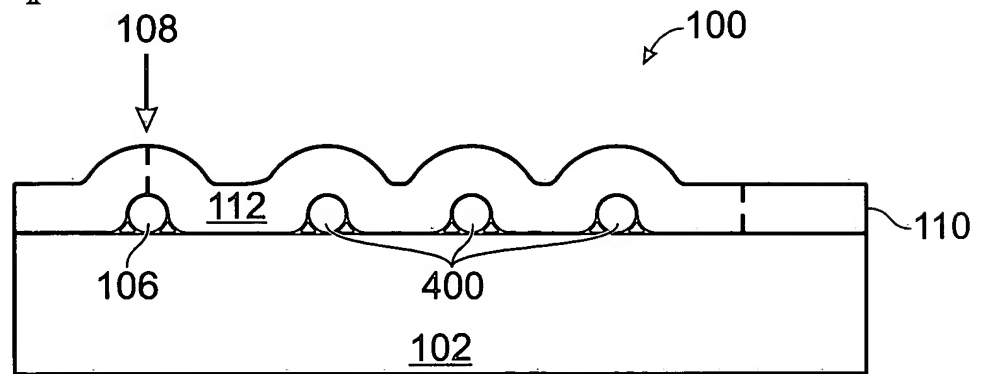


Fig. 5

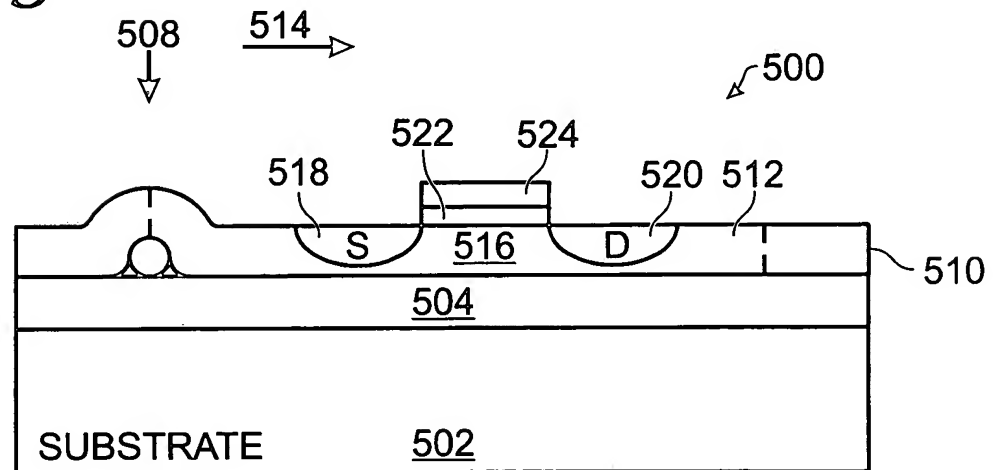


Fig. 6

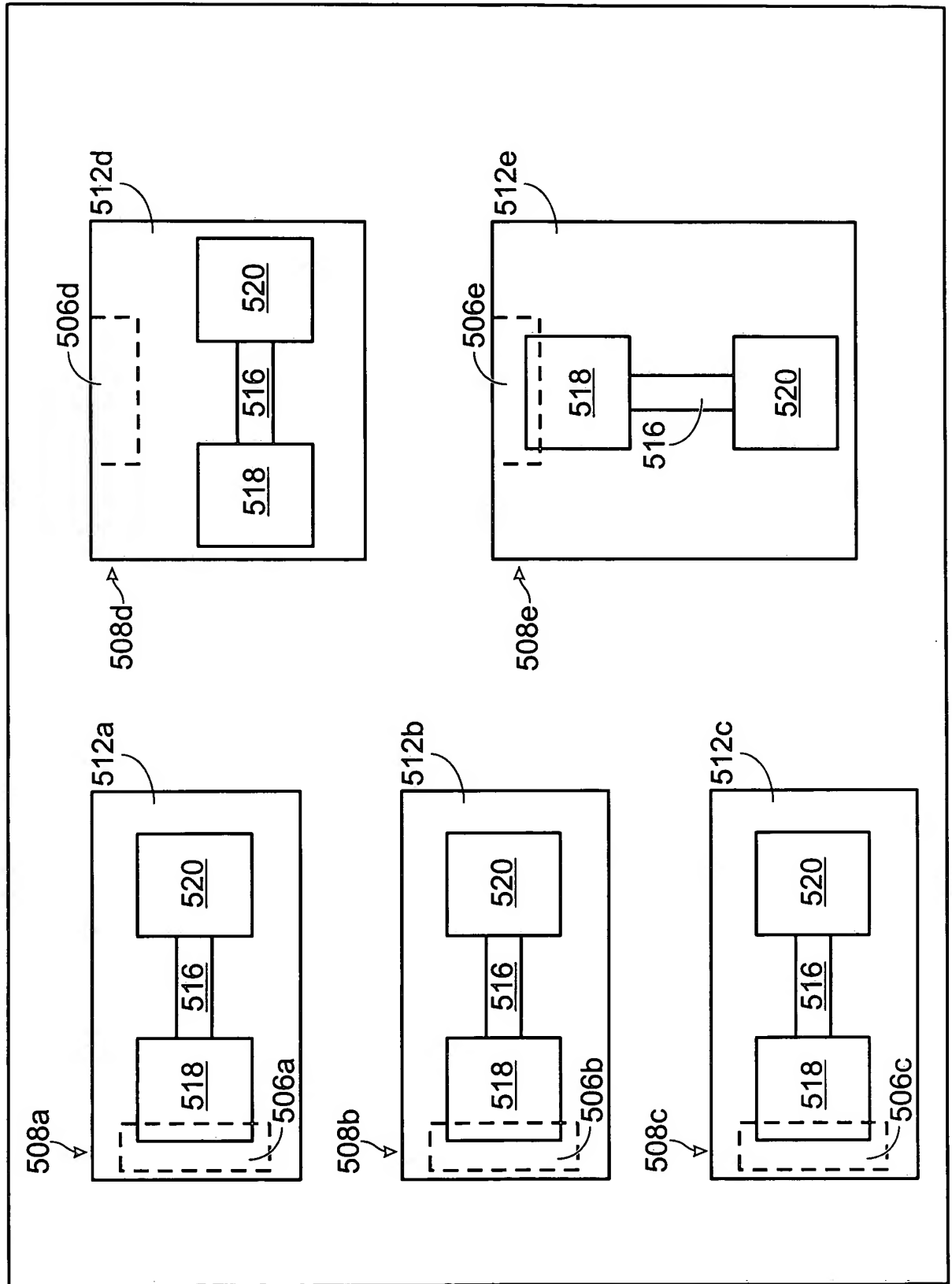


Fig. 7

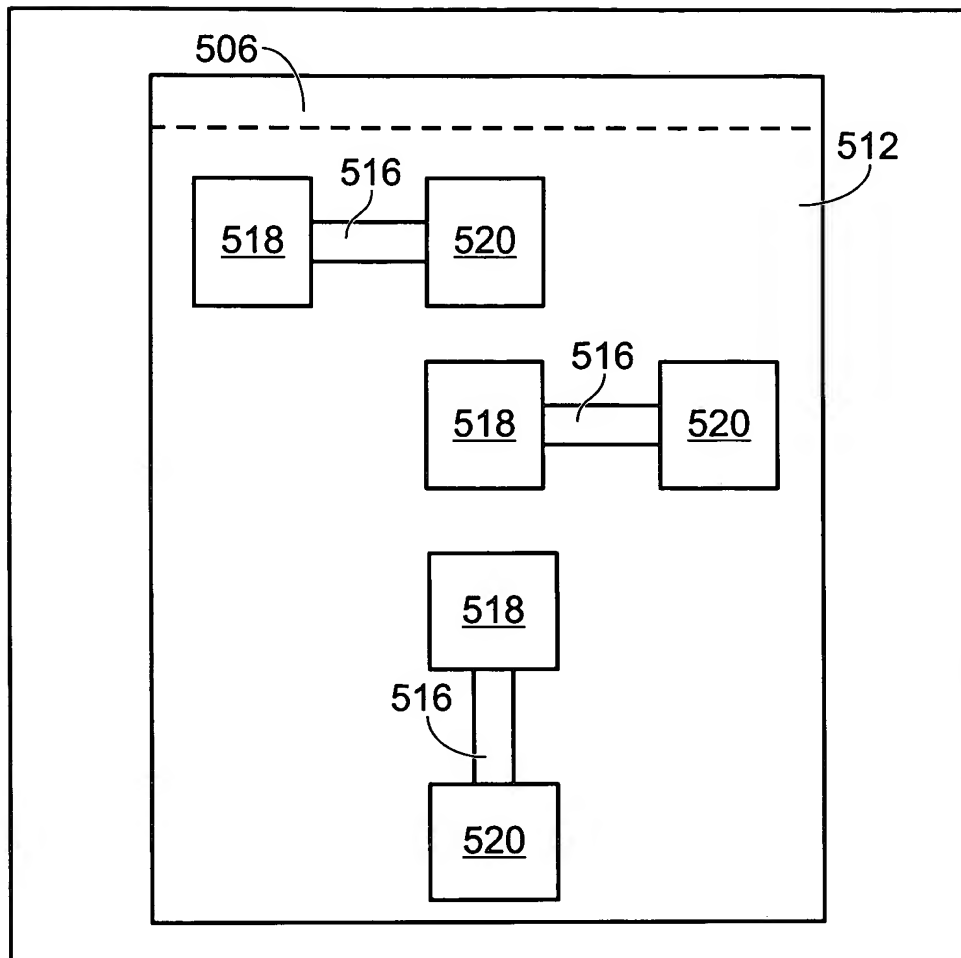
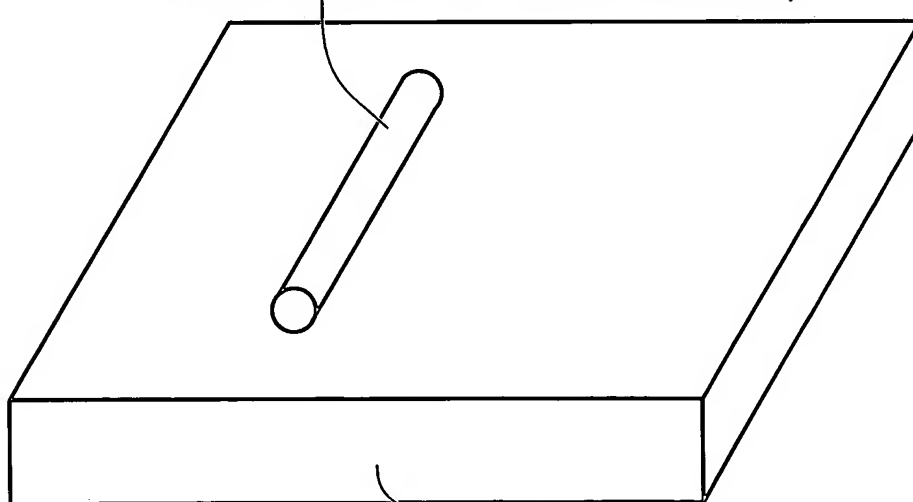


Fig. 8

SEMICONDUCTOR "SEED" (i.e. Si NANOWIRE:
~10nm DIAMETER, ~100um LENGTH)



SUBSTRATE (WITH OR WITHOUT ADDITIONAL COATINGS)

Fig. 9

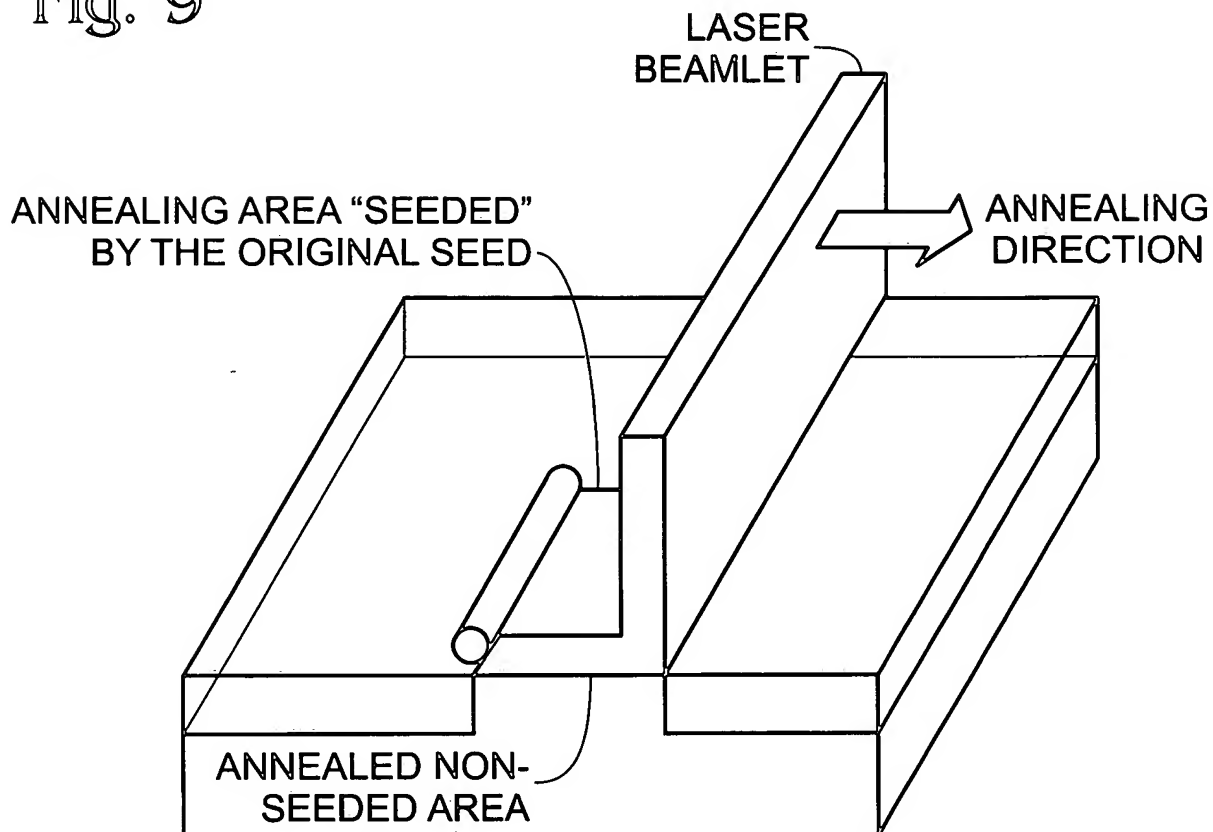


Fig. 11

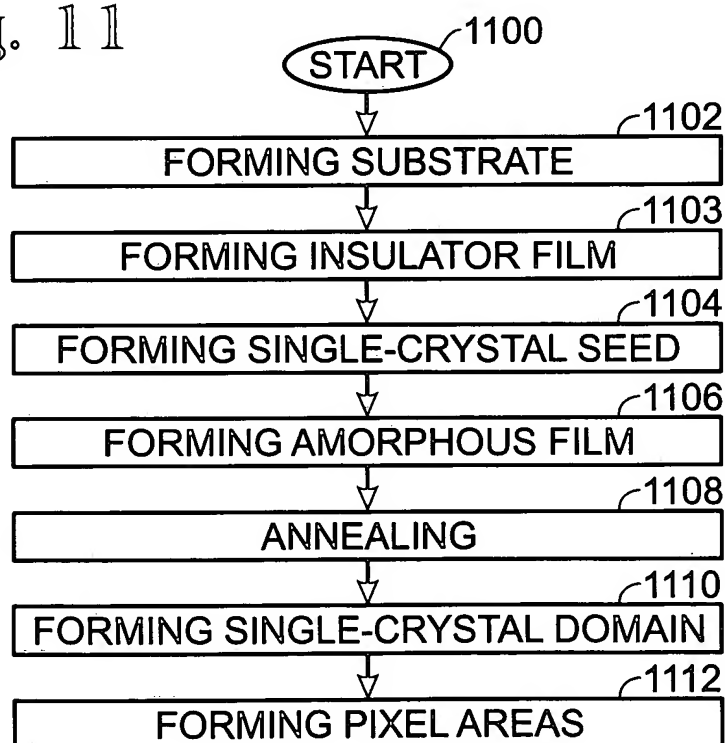


Fig. 10

